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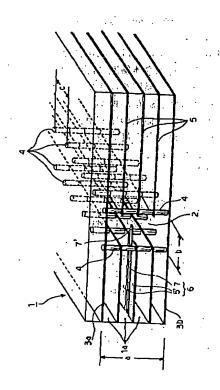
(54) 【発明の名称】誘電体導波管線路の結合構造

(57)【要約】

【課題】誘電体の内部に疑似的な導波管を形成した誘電 体導波管線路を他の伝送線路と組み合わせて用いること ができなかった。

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【解決手段】誘電体基板1の少なくとも一部を挟んで対 向する一対の主導体層 3 a , 3 b と、該一対の主導体層 3 a, 3 b 間を電気的に接続し、電気信号の伝達方向に 遮断波長の1/2以下の問隔で配設された二列のバイア ホール4群とを具備し、前記主導体層3 a, 3 b 及びバ イアホール4群で囲まれる導波管領域によって電気信号 を伝達する誘電体導波管線路2と、他の伝送線路6とを 結合させるための構造であって、前記誘電体導波管線路 2のバイアホール4群で形成された側面もしくは端面を 介して該誘電体導波管線路2の内部に他の伝送線路6の 線路導体7の一端7'を挿入する。



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【特許請求の範囲】

【請求項1】誘電体基板の少なくとも一部を挟んで対向する一対の主導体層と、該一対の主導体層間を電気的に接続し、電気信号の伝達方向に遮断波長の1/2以下の間隔で配設された二列のバイアホール群とを具備し、前記主導体層及びバイアホール群で囲まれる導波管領域によって電気信号を伝達する誘電体導波管線路と、他の伝送線路とを結合させるための構造であって、前記誘電体導波管線路のバイアホール群で形成された側面もしくは端面を介して該誘電体導波管線路の内部に他の伝送線路の線路導体の一端を挿入してなることを特徴とする積層型導波管線路の結合構造。

【請求項2】前記他の伝送線路がコプレーナ線路、もしくはストリップ線路であることを特徴とする請求項1に 記載の誘電体導波管線路の結合構造。

【発明の詳細な説明】

[0001]

【発明の属する技術分野】本発明は、マイクロ波やミリ 被等の高周波の電気信号を伝達するための誘電体導波管 線路と他の伝送線路との結合構造に関するものである。

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[0002]

【従来技術及びその課題】従来より、配線基板や高周波パッケージ等の内部に構成される高周波信号用(周波数が1GHz以上)の伝送線路として、ストリップ線路やマイクロストリップ線路、コプレーナ線路等が知られている。これらの伝送線路は、複数の誘電体層上に線路となる導体を印刷してこれらを積層するといった積層化技術によって比較的簡単に形成することができ、マイクロ波領域の周波数をもった電気信号の伝送に優れた特性を有していることから、高周波信号用の伝送線路として幅 30 広く用いられている。

広く用いられている。 【0003】また最近では、誘電体基板を挟む一対の主導体層と、これら主導体層間に配設される二列のバイアホール群とによって疑似的な矩形導波管を形成するようにした誘電体導波管線路が提案されており(特開平6-53711号公報参照)、従来の矩形導波管と同等の優れた伝送特性と高い生産性とを併せ持った新しいタイプの伝送線路として注目されている。

【0004】そして、このような種々の伝送線路は、その伝送特性や用途等に応じて適宜使い分けられており、そのため、配線基板等の内部に種類の異なる伝送線路を複数形成してこれらを相互に接続させる技術が要求されている。

【0005】ところが、種類の異なる伝送線路同士を相互に接続させる場合、例えば、ストリップ線路とマイクロストリップ線路であれば両者の端部同士を電磁結合させる等して比較的簡単に接続することができるものの、上述のような誘電体導波管線路と他の伝送線路との結合構造についてはこれまで具体的な検討がなされておらず、誘電体導波管線路と他の伝送線路とを組み合わせて50

用いることはできなかった。

[0006]

【課題を解決するための手段】そこで発明者らは、誘電 体導波管線路と他の伝送線路との結合構造について検討 を重ねた結果、誘電体導波管線路の側面もしくは端面を 介して他の伝送線路の一部を挿入させ、これを電磁気的 に結合させることで直接、接続できることを見出した。 【0007】即ち、本発明の誘電体導波管線路の結合構 造は、誘電体基板の少なくとも一部を挟んで対向する一 対の主導体層と、該一対の主導体層間を電気的に接続 し、電気信号の伝達方向に遮断波長の1/2以下の間隔 で配設された二列のバイアホール群とを具備し、前記主 導体層及びバイアホール群で囲まれる導波管領域によっ て電気信号を伝達する誘電体導波管線路と、他の伝送線 路とを結合させるための構造であって、前記誘電体導波 管線路のバイアホール群で形成された側面もしくは端面 を介して該誘電体導波管線路の内部に他の伝送線路の線 路導体の一端を挿入してなることを特徴とするものであ

【0008】また本発明の積層型導波管線路の結合構造は、前記他の伝送線路がコプレーナ線路、もしくはストリップ線路であることを特徴とするものである。

[0009]

【発明の実施の形態】以下、本発明を添付図面に基づいて詳細に説明する。図1は本発明の誘電体導波管線路の結合構造に係る一形態を示す概略斜視図であり、1は誘電体基板、2は誘電体導波管線路、3a、3bは一対の主導体層、4はバイアホール、6は他の伝送線路としてのコプレーナ線路、7はコプレーナ線路6の線路導体である。

【0010】前記誘電体基板1は例えばアルミナセラミックスやガラスセラミックス, 窒化アルミニウムセラミックス等の誘電体材料から成り、例えばアルミナセラミックスから成る場合、アルミナ、シリカ、マグネシア等のセラミックス原料粉末に適当な有機溶剤、溶媒を添加混合して泥漿状に成すとともにこれを従来周知のドクターブレード法やカレンダーロール法等を採用してシート状となすことによって複数枚のセラミックグリーンシートを得、しかる後、前記セラミックグリーンシートの各々に適当な打ち抜き加工を施すとともにこれらを上下に積層し、高温(約1600℃)で焼成することによって製作される。

【0011】また前記誘電体基板1の内部には誘電体導 波管線路2とコプレーナ線路6とがそれぞれ配設されて おり、この2つの伝送線路は誘電体基板1内で相互に結 合されている。前記誘電体導波管線路2は、誘電体基板 1の少なくとも一部、例えば誘電体基板1を形成する5 層の誘電体層1aのうち、下の4層を挟んで対向する一 対の主導体層3a,3bと、該一対の主導体層3a,3 b間を電気的に接続し、電気信号の伝達方向に遮断波長

 (λc) の1/2以下の間隔 c で配設された二列のバイ アホール群 (パイアホール4の径: φ50~300μ m) と、前記一対の主導体層3a,3b間に各バイアホ ール4と接続した状態で配置された3層の副導体層5と で形成されている。

【0012】このような誘電体導波管線路2は一対の主 導体層3a, 3b間に形成されている2つのバイアホー ル群が電気信号の伝達方向に遮断波長(λ c)の1/2 以下の間隔 c で配列しており、電気信号の伝達方向に対 し垂直な方向に電気的な壁を形成していることから、電 10 磁波を線路の形成方向にのみ良好に伝播させることがで きる。よって、一対の主導体層3a,3bと二列のバイ アホール群とで囲まれる領域を a × b (a:一対の主導 体層 3 a , 3 b 間の距離、b: 二列のバイアホール群間 の距離) の擬似的な矩形導波管として作用させることが でき、マイクロ波やミリ波等の高周波の電気信号を伝達 するのに適した伝送線路として用いることができる。例 えば、誘電体導波管線路2を伝播する電磁波のモードが TE10モードである場合、一対の主導体層3a,3b 間の距離 a を電気信号の中心波長の 1/2よりもやや大 20 きく、また二列のバイアホール群間の距離 b を a / 2.程 度に設定し、一対の主導体層 3 a , 3 b を形成した面が 電界と平行なE面になし、またバイアホール4や副導体 層5で囲まれた面が磁界と平行なH面になす。

【0013】ここで、前記バイアホール4の配列ピッチ c を遮断波長 (λc) の1/2以下になすのは、配列ピ ッチ c が遮断波長 (λc) の 1/2 よりも大きくなる と、誘電体導波管線路2に電磁波を供給する際、隣接す るバイアホール4間より電磁波が漏れ、電気信号が誘電 体導波管線路2に沿って良好に伝播しなくなるからであ り、従ってバイアホール4の配列ピッチ c は遮断波長 (λ c) の1/2以下に設定する必要がある。 ^

【0014】尚、前記副導体層5は、誘電体導波管線路 2の側壁をより細かな格子状になして電磁波の遮断効果 を向上させるためのものであり、誘電体導波管線路2を 構成するのに不可欠な構成要素ではない。

【0015】一方、前記誘電体基板1の内部に誘電体導 波管線路2と共に配設されているコプレーナ線路6は、 帯状の線路導体7と、前記副導体層5の一部を利用して 線路導体7の両側に配される接地導体5.とで構成され ており、電気信号を前記線路導体7を介して伝播させる ようになっている。

【0016】このようなコプレーナ線路6は、誘電体基 板1を構成する誘電体層1aの一表面上に形成されてお り、例えば、特性インピーダンスを50Ωとなすよう に、線路導体7の導体幅を50~500 μmに、また線 路導体7と接地導体との間の距離を50~500μmに 設定する。

【0017】かかるコプレーナ線路6や前述の誘電体導

アホール4、副導体層5及び線路導体7は、誘電体基板 1がアルミナセラミックスからなる場合、タングステン やモリブデン等の髙融点金属材料により形成され、誘電 体基板 1 を製作する際に誘電体基板 1 の内部に同時に配 設される。即ち、誘電体基板1となる複数のセラミック グリーンシートの表面に、タングステン、モリブデン等 の金属粉末を含む導電ペーストを従来周知の厚膜印刷法 等を採用することによって5~25μmの厚みをもって 所定パターンに印刷・塗布するとともに、セラミックグ リーンシートに予め開けておいた穴内に導電ペーストを 埋め込み、セラミックグリーンシートと同時に焼成する ことによって誘電体基板1の内部に誘電体導波管線路2 及びコプレーナ線路6が形成される。

【0018】そして、このような誘電体導波管線路2と コプレーナ線路6は、図2、図3に示すように、コプレ 一ナ線路6の線路導体7の一端(以下、スタブという) を誘電体導波管線路2のバイアホール群で形成された側 面を介して誘電体導波管線路2の内部に挿入させること により結合がなされている。

【0019】かかる結合構造によれば、コプレーナ線路 6を伝播してきた電気信号は、線路導体7個スタブ7 を励振させ、スタブ7'を中心とした同心円状の磁界を 発生する。そこで前述した誘電体導波管線路2における 二列のバイアホール群間の距離 b を a / 2 に設定する と、伝播するTE10モードと結合し、誘電体導波管線 路2とコプレーナ線路6との接続が良好に行われること 大师 的复数人员的 医紫色精度 医二氏 となる。

【0020】尚、前記スタブ7」の長さdを、伝播させ る電気信号の中心波長の1/4に設定すると、スタブ 30 7 が1/4波長のモノポールアンテナと同様の作用を なすので中心波長の電気信号をより効率的に誘電体導波 管線路2内に放射させることができる。ただし、実際に は図2の右側の接地導体に対してキャパシタンスが、下 側の接地導体に対してインダクタンスが発生するので、 その分を考慮してスタブ7'の長さdを微調整する必要 があり、このため、スタブワ'の長さdはa/4~a/ 3に設定するのが好ましい。

【0021】また誘電体導波管線路2の端面とスタブ 7'の中心との間の距離 e を、伝播させる電気信号の管 内波長の11/4に設定すると、端面で反射されて逆位相 となった電磁波は反射されずに進んだ電磁波と同位相で 加わるので良好な特性が得られるようになる。ただし、 実際には図2の右側の接地導体に対してキャパシタンス が、下側の接地導体に対してインダクタンスが発生する ので、その分を考慮して誘電体導波管線路2の端面とス タブ7'の中心との間の距離 e を微調整する必要があ り、このため、前記距離eはa/4~a/3に設定する のが好ましい。

【0022】尚、本発明は上述した形態に限定されるも 波管線路2を構成する一対の主導体層3a,3b、バイ 50 のではなく、本発明の要旨を逸脱しない範囲において種





々の変更、改良等が可能であり、例えば上述の形態では 他の伝送線路としてコプレーナ線路を用いたが、これに 代えて、図4のように、帯状の線路導体8と副導体層 5'の一部とで構成されるストリップ線路6'を用いて も良い。この場合、ストリップ線路 6'を伝播してきた 電磁波はスタブ8'を励振させて同心円状の磁界を発生 するため、これを誘電体導波管線路2を伝播するモード と結合させることにより伝送線路同士の結合を良好に行 うことができる。また上述の形態では、誘電体導波管線 路2内に挿入されるコプレーナ線路6の線路導体7のス 10 タブ7′を線路導体7の他の部分と略等しい幅になした が、これに代えて、図5に示すように、誘電体導波管線 路内に挿入されるコプレーナ線路等の他の伝送線路の線 路導体9のスタブ9'を線路導体9の他の部分よりも幅 広になしておけば、コプレーナ線路と誘電体導波管線路 との特性インピーダンスの不一致を有効に緩和して伝送 特性を向上させることができる。従って誘電体導波管線 路内に挿入される他の伝送線路の線路導体9のスタブ 9'を線路導体9の他の部分よりも幅広になしておくこ ··とが好ましい。 - 13: - 1 / 20 / 4.2 / 1 / 1 / 1 / 1 / 1 / 20

【0023】更に上述の形態では、コプレーナ線路6の 線路導体7のスタブ7'をバイアホール群で形成された 誘電体導波管線路2の側面を介して誘電体導波管線路2 の内部に挿入させるようにしたが、これに代えて、図6 に示すように、コプレーナ線路等の他の伝送線路の線路 導体10のスタブ10、を誘電体導波管線路2の端面を 介して誘電体導波管線路2の内部に挿入させるようにし ても構わない。この場合、TE11モードの電磁波と結 合させることによって電磁界との結合が良好に行われ る。

[0024]

【発明の効果】本発明によれば、誘電体導波管線路と他 の伝送線路とを簡単かつ良好に接続することができ、一 個の配線基板等の内部で誘電体導波管線路を他の伝送線 路と組み合わせて用いることが可能になる。

【図面の簡単な説明】

【図1】本発明の誘電体導波管線路の結合構造の一形態 を示す概略斜視図である。 🤲

【図2】他の伝送線路が設けられている誘電体層の一主 面を示す平面図である。

【図3】図2のX-X線断面図である。

【図4】 本発明の誘電体導波管線路の結合構造の他の形 態を示す概略斜視図である。

【図5】本発明の誘電体導波管線路の結合構造の他の形 態を示す平面図である。

【図6】本発明の誘電体導波管線路の結合構造の他の形 態を示す平面図である。

【符号の説明】

1・・・・・誘電体基板

2・・・・・・誘電体導波管線路

3a,3b ・・・・一対の主導体層

4・・・・・・バイアホール

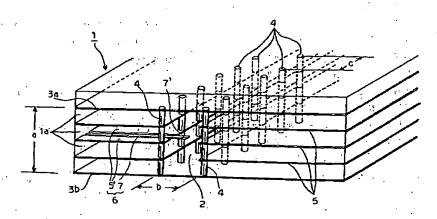
5,5'・・・・・副導体層

6,6'・・・・他の伝送線路

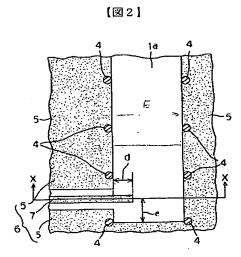
7.8.9.10・・・線路導体

7': 8', 9', 10'・・線路導体の一端

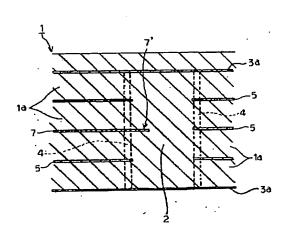
【図1】



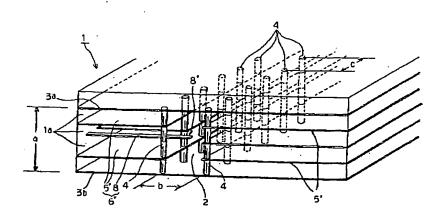




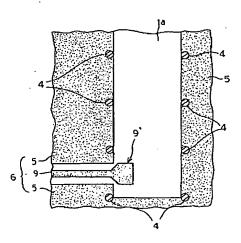
[図3]



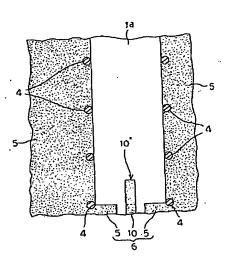
[図4]



【図5】



【図6】



10/526105

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Bibliography

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(51) [International Patent Classification (6th Edition)]
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3/00
3/12
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H01P 5/107
                  В
3/00
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[Request for Examination] Un-asking.
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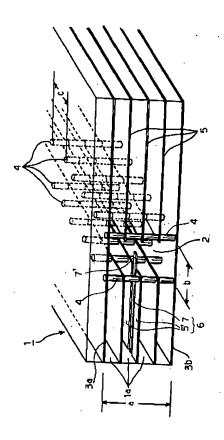
Epitome

(57) [Abstract]

[Technical problem] The dielectric-waveguide track in which the false waveguide was formed was not able to be used for the interior of a dielectric combining other transmission lines.

[Means for Solution] The initiative body whorls 3a and 3b of the pair which counters on both sides of some dielectric substrates [at least] 1, Connect electrically between initiative body whorl 3a of this pair, and 3b, and Bahia hall 4 group of two trains arranged in the transfer direction of an electrical signal or less [of cutoff wave length] at intervals of 1/2 is provided. The dielectric-waveguide track 2 which transmits an electrical signal by the waveguide field surrounded by said initiative body whorls 3a and 3b and Bahia hall 4 group, It is the structure for combining other transmission lines 6, and end 7' of the line conductor 7 of other transmission lines 6 is inserted in the interior of this dielectric-waveguide track 2 through the side face or end face formed by Bahia hall 4 group of said dielectric-waveguide track 2.

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CLAIMS

[Claim(s)]

[Claim 1] Between the initiative body whorl of the pair which counters on both sides of some dielectric substrates [at least], and the initiative body whorl of this pair is connected electrically. The dielectric-waveguide track which transmits an electrical signal by the waveguide field which possesses the Bahia hall group of two trains

arranged in the transfer direction of an electrical signal or less [of cutoff wave length] at intervals of 1/2, and is surrounded by said initiative body whorl and the Bahia hall group, Joint structure of the laminating mold waveguide track characterized by coming to insert the end of the line conductor of other transmission lines in the interior of this dielectric-waveguide track through the side face or end face which is the structure for combining other transmission lines, and was formed by the Bahia hall group of said dielectric-waveguide track.

[Claim 2] Joint structure of the dielectric-waveguide track according to claim 1 characterized by the transmission line besides the above being a KOPURENA track or the strip line.

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DETAILED DESCRIPTION

[Detailed Description of the Invention] [0001]

[Field of the Invention] This invention relates to the joint structure of the dielectric-waveguide track for transmitting the electrical signal of RFs, such as microwave and a millimeter wave, and other transmission lines.

[0002]

[Description of the Prior Art] Conventionally, the strip line, a microstrip line, a KOPURENA track, etc. are known as the transmission line for high frequency signals (a frequency is 1GHz or more) constituted by the interior, such as a wiring substrate and a high frequency package. On two or more dielectric layers, these transmission lines can print the conductor used as a track, can be formed comparatively easily with the lamination technique of carrying out the laminating of these, and since it has the property excellent in

transmission of an electrical signal with the frequency of a microwave range, they are broadly used as the transmission line for RF signals. [0003] Moreover, recently, the dielectric-waveguide track in which false rectangular waveguide was formed is proposed by the initiative body whorl of the pair whose dielectric substrate is pinched, and the Bahia hall group of two trains arranged between these initiative body whorls (refer to JP, 6-53711, A), and it is observed as the transmission line having the outstanding transmission characteristic equivalent to the conventional rectangular waveguide, and high productivity new type. [0004] And the technique of forming two or more transmission lines where such the various transmission lines are suitably used properly according to the transmission characteristic, application, etc., therefore classes differ in the interior, such as a wiring substrate, and connecting these mutually is demanded.

[0005] However, when the transmission lines from which a class differs were connected mutually, an examination concrete about the joint structure of the above dielectric-waveguide tracks of what can carry out carrying out the electromagnetic coupling of both edges etc., and can be connected comparatively easily if it is the strip line and a microstrip line, and other transmission lines until now was not made, and it was not able to use combining a dielectric-waveguide track and other transmission lines.

[0006]

[Means for Solving the Problem] Then, artificers found out that it was connectable directly by making a part of other transmission lines insert through the side face or end face of a dielectric-waveguide track, and combining this in electromagnetism as a result of repeating examination about the joint structure of a dielectric-waveguide track and other transmission lines.

[0007] Namely, the joint structure of the dielectric-waveguide track of this invention Between the initiative body whorl of the pair which counters on both sides of some dielectric substrates [at least], and the initiative body whorl of this pair is connected electrically. The dielectric-waveguide track which transmits an electrical signal by the waveguide field which possesses the Bahia hall group of two trains arranged in the transfer direction of an electrical signal or less [of cutoff wave length] at intervals of 1/2, and is surrounded by said initiative body whorl and the Bahia hall group, It is the structure for combining other transmission lines, and is characterized by coming to insert the end of the line conductor of other transmission lines in the interior of this dielectric-waveguide track through the side face or end

face formed by the Bahia hall group of said dielectric-waveguide track. [0008] Moreover, joint structure of the laminating mold waveguide track of this invention is characterized by the transmission line besides the above being a KOPURENA track or the strip line. [0009]

[Embodiment of the Invention] Hereafter, this invention is explained to a detail based on an accompanying drawing. Drawing 1 is the outline perspective view showing one gestalt concerning the joint structure of the dielectric-waveguide track of this invention, and, for 1, as for a dielectric-waveguide track, and 3a and 3b, a dielectric substrate and 2 are [the initiative body whorl of a pair, the KOPURENA track as the transmission line of others / 4 / 6 / the Bahia hall and /, and 7] the line conductors of the KOPURENA track 6.

[0010] Said dielectric substrate 1 consists of dielectric materials, such as alumina ceramics, and crystallized glass, alumimium nitride ceramics. When it consists of alumina ceramics, for example, the suitable organic solvent for ceramic raw material powder, such as an alumina, a silica, and a magnesia, The ceramic green sheet of two or more sheets is obtained by adopting a well-known doctor blade method, the well-known calendering roll method, etc. conventionally, and making this with the shape of a sheet, while carrying out addition mixing of the solvent and accomplishing in the shape of slurry. While performing suitable punching processing for each of said ceramic green sheet after an appropriate time, the laminating of these is carried out up and down, and it is manufactured by calcinating at an elevated temperature (about 1600 degrees C).

[0011] Moreover, the dielectric-waveguide track 2 and the KOPURENA track 6 are arranged in the interior of said dielectric substrate 1, respectively, and these two transmission lines are mutually combined within the dielectric substrate 1. Said dielectric-waveguide track 2 The initiative body whorls 3a and 3b of the pair of the dielectric substrate 1 which counters on both sides of four lower layers in part among dielectric layer of five layers 1a which forms the dielectric substrate 1 at least, The Bahia hall group of two trains which connected electrically between initiative body whorl 3a of this pair, and 3b, and were arranged in the transfer direction of an electrical signal or less [of cutoff wave length (lambdac)] at intervals of [c] 1/2 (the path of the Bahia hall 4: phi50-300micrometer), It is formed between initiative body whorl 3a of said pair, and 3b by the subconductor layer 5 of three layers arranged in the condition of having connected with each Bahia hall 4.

[0012] Since two Bahia hall groups currently formed between initiative body whorl 3a of a pair and 3b have arranged in the transfer direction of an electrical signal or less [of cutoff wave length (lambdac)] at intervals of [c] 1/2 and form the electric wall in the perpendicular direction to the transfer direction of an electrical signal, such a dielectric-waveguide track 2 can make an electromagnetic wave spread only in the formation direction of a track to fitness. Therefore, the field surrounded by the initiative body whorls 3a and 3b of a pair and the Bahia hall group of two trains can be made to be able to act as false rectangular waveguide of axb (a: distance between initiative body whorl 3a of a pair, and 3b, distance of b:2 train Bahia hall between groups), and it can use as the transmission line suitable for transmitting the electrical signal of RFs, such as microwave and a millimeter wave. For example, when the mode of the electromagnetic wave which spreads the dielectric-waveguide track 2 is the TE10 mode, Are a little larger than one half of the main wavelength of an electrical signal in the distance a between initiative body whorl 3a of a pair, and 3b. Moreover, the distance b of two trains Bahia hall between groups is set about to a/2, and it makes to an H plane with the field parallel to a field where the field in which the initiative body whorls 3a and 3b of a pair were formed was surrounded by the Eth page parallel to electric field by nothing, and the Bahia hall 4 and the subconductor layer 5. [0013] Making array-pitch c of said Bahia hall 4 or less [of cutoff wave length (lambdac)] to 1/2 here If array-pitch c becomes larger than one half of cutoff wave length (lambdac), when an electromagnetic wave will be supplied to the dielectric-waveguide track 2, It is because an electromagnetic wave meets leakage, an electrical signal meets the dielectric-waveguide track 2 and it stops spreading good from between the adjoining Bahia-hall 4, therefore it is necessary to set array-pitch c of the Bahia hall 4 or less [of cutoff wave length (lambdac)] to 1/2. [0014] In addition, said subconductor layer 5 is for making the side attachment wall of the dielectric-waveguide track 2 to the shape of a finer grid, and raising the screening effect of an electromagnetic wave, and is not a component indispensable to constitute the dielectricwaveguide track 2.

[0015] the touch-down to which the KOPURENA track 6 currently arranged in the interior of said dielectric substrate 1 with the dielectric-waveguide track 2 is allotted to the both sides of a line conductor 7 on the other hand using the band-like line conductor 7 and said a part of subconductor layer 5 -- a conductor -- it consists of 5' and an electrical signal is made to spread through said line conductor 7

[0016] such a KOPURENA track 6 is formed on the 1 front face of dielectric layer 1a which constitutes the dielectric substrate 1, for example, makes a characteristic impedance with 50 ohms -- as -- the conductor width of a line conductor 7 -- 50-500 micrometers -- moreover, a line conductor 7 and touch-down -- the distance between conductors is set as 50-500 micrometers.

[0017] When the dielectric substrate 1 consists of alumina ceramics, the initiative body whorls 3a and 3b, the Bahia hall 4, the subconductor layer 5, and line conductor 7 of the pair which constitutes this KOPURENA track 6 and above-mentioned dielectric-waveguide track 2 are formed with refractory metal ingredients, such as a tungsten and molybdenum, and in case they manufacture the dielectric substrate 1, they are arranged in the interior of the dielectric substrate 1 by coincidence. On namely, the front face of two or more ceramic green sheets used as the dielectric substrate 1 While having the thickness of 5-25 micrometers and printing and applying at a predetermined pattern by adopting the conductive paste containing metal powder, such as a tungsten and molybdenum, for well-known thick film printing etc. conventionally Conductive paste is embedded in the hole beforehand made . in the ceramic green sheet, and the dielectric-waveguide track 2 and the KOPURENA track 6 are formed in the interior of the dielectric substrate 1 by calcinating to a ceramic green sheet and coincidence. [0018] And association is made when such the dielectric-waveguide track 2 and the KOPURENA track 6 make the end (henceforth a stub) of the line conductor 7 of the KOPURENA track 6 insert in the interior of the dielectric-waveguide track 2 through the side face formed by the Bahia

[0019] According to this joint structure, the electrical signal which has spread the KOPURENA track 6 excites stub 7' of a line conductor 7, and generates the field of the shape of a concentric circle centering on stub 7'. Then, when the distance b of two trains in the dielectric—waveguide track 2 mentioned above Bahia hall between groups is set as a/2, it will combine with the TE10 mode to spread and connection between the dielectric—waveguide track 2 and the KOPURENA track 6 will be made good.

hall group of the dielectric-waveguide track 2 as shown in drawing 2 and

drawing 3.

[0020] In addition, if die-length d of said stub 7' is set as one fourth of the main wavelength of the electrical signal made to spread, since stub 7' will make the same operation as the monopole antenna of quarter-wave length, the electrical signal of main wavelength can be made to emit in the dielectric-waveguide track 2 more efficiently. however --

actual -- the touch-down on the right-hand side of drawing 2 -- a conductor -- receiving -- capacitance -- lower touch-down -- since an inductance occurs to a conductor, it is necessary to tune die-length d of stub 7' finely in consideration of that part, and, as for die-length d of stub 7', for this reason, it is desirable to set it as a / 4 - a/3. [0021] Moreover, if it sets as one fourth of the guide wave lengths of the electrical signal which makes the distance e between the end face of the dielectric-waveguide track 2, and the core of stub 7' spread, since the electromagnetic wave which was reflected by the end face and became an opposite phase is in phase and is added with the electromagnetic wave which progressed without being reflected, a good property will come to be acquired. however -- actual -- the touch-down on the right-hand side of drawing 2 -- a conductor -- receiving -- capacitance -- lower touchdown -- since an inductance occurs to a conductor, it is necessary to tune the distance e between the end face of the dielectric-waveguide track 2, and the core of stub 7' finely in consideration of that part, and, as for said distance e, for this reason, it is desirable to set it as a / 4 - a/3.

[0022] in addition -- this invention -- having mentioned above -- a gestalt -- limiting -- having -- a thing -- it is not -- this invention -- a summary -- not deviating -- the range -- setting -- versatility -modification -- amelioration -- etc. -- possible -- for example, -- a **** -- a gestalt -- **** -- others -- the transmission line -- ***** -- KOPURENA -- a track -- having used -- although -- this -- replacing with -- drawing 4 -- like -- band-like -- a line conductor -- eight -secondary -- a conductor layer -- five -- ' -- a part -- constituting -having -- the strip line -- six -- ' -- you may use . In this case, since the electromagnetic wave which has spread strip-line 6' excites stub 8' and generates a concentric circle-like field, it can combine the transmission lines good by combining this with the mode which spreads the dielectric-waveguide track 2. moreover, stub 7' of the line conductor 7 of the KOPURENA track 6 inserted into the dielectricwaveguide track 2 with an above-mentioned gestalt -- other parts of a line conductor 7, and abbreviation, although made to equal width of face If stub 9' of the line conductor 9 of other transmission lines, such as a KOPURENA track inserted into a dielectric-waveguide track, is made more broadly than other parts of a line conductor 9 as it replaces with this and is shown in drawing 5 The inequality of the characteristic impedance of a KOPURENA track and a dielectric-waveguide track can be eased effectively, and a transmission characteristic can be raised. Therefore, it is desirable to make more broadly than other parts of a

line conductor 9 stub 9' of the line conductor 9 of other transmission lines inserted into a dielectric-waveguide track.

[0023] furthermore -- a **** -- a gestalt -- **** -- KOPURENA -- a track -- six -- a line conductor -- seven -- a stub -- seven -- ' -- Bahia -- a hole -- a group -- forming -- having had -- a dielectric waveguide -- a track -- two -- a side face -- minding -- a dielectric waveguide -- a track -- two -- the interior -- inserting -- making -- having made -- although -- this -- replacing with -- drawing 6 -- being shown -- as -- KOPURENA -- a track -- etc. -- others -- the transmission line -- a line conductor -- ten -- a stub -- ten -- ' -- a dielectric waveguide -- a track -- two -- an end face -- minding -- a dielectric waveguide -- a track -- two -- the interior -- inserting -- making -- you may make . In this case, association with electromagnetic field is performed good by making it combine with the electromagnetic wave in the TE11 mode. [0024]

[Effect of the Invention] According to this invention, a dielectric-waveguide track and other transmission lines can be connected simply and good, and it becomes possible to use a dielectric-waveguide track combining other transmission lines in the interior, such as a wiring substrate of a piece.

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DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] It is the outline perspective view showing one gestalt of the joint structure of the dielectric-waveguide track of this invention. [Drawing 2] It is the top view showing one principal plane of a dielectric layer in which other transmission lines are established. [Drawing 3] It is X-X-ray sectional view of drawing 2.

[Drawing 4] It is the outline perspective view showing other gestalten of the joint structure of the dielectric-waveguide track of this invention.

[Drawing 5] It is the top view showing other gestalten of the joint structure of the dielectric-waveguide track of this invention.

[Drawing 6] It is the top view showing other gestalten of the joint structure of the dielectric-waveguide track of this invention.

[Description of Notations]

1 Dielectric substrate

2 Dielectric-waveguide track

3a and 3b Initiative body whorl of a pair

4 Bahia hall

5 5' Subconductor layer

6 and 6' -- the transmission line besides

7, 8, 9, 10 Line conductor

7', 8', 9', and 10 --- ' .. the end of a line conductor

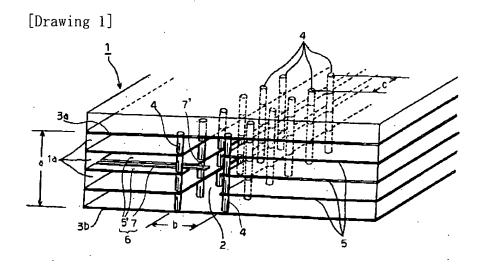
[Translation done.]

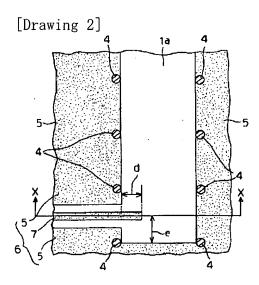
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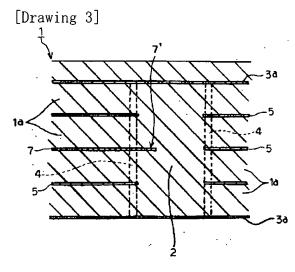
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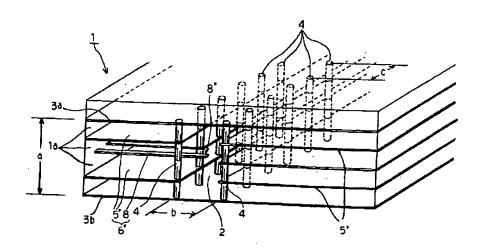
DRAWINGS



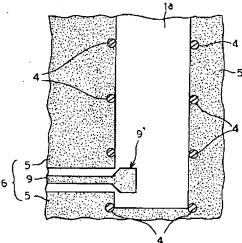




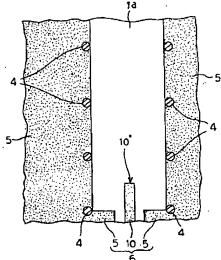
[Drawing 4]



[Drawing 5]



[Drawing 6]



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